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\*\* CONTINUING DATA \*\*\*\*\* *yes sn 6*

This application is a CIP of 09/292,655 04/15/1999 PAT 6,363,295  
 which is a CON of 08/871,015 06/06/1997 PAT 5,907,492

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *none*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 6	TOTAL CLAIMS 108 / 102	INDEPENDENT CLAIMS 11
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>Steven R. Garland</i> Examiner's Signature Initials				

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## TITLE

Method for using data regarding manufacturing procedures integrated circuits (IC'S) have undergone, such as repairs, to select procedures the IC'S will undergo, such as additional repairs

☐ All Fees

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